

In the Specification

Please replace the following paragraphs, as noted.

(Page 9, line 10)

FIG. 4 is a diagram illustrating a constrained STD corridor defined relative to the data stream in accordance with the present invention. The constrained STD corridor exists between lower limit 401 and upper limit 402 and extends over time as the lower limit 401 and the upper limit 402 change, wherein the lower limit 401 may also be referred to as a lowest bit occurrence and the upper limit 402 may also be referred to as a highest bit occurrence. The distance between the upper limit 402, the highest bit occurrence, and the lower limit 401, the lowest bit occurrence, is preferably equal to the STD buffer size minus the VBV buffer size. While the lower limit 401, the lowest bit occurrence, and the upper limit 402, the highest bit occurrence, may appear to extend as straight lines over time, they may vary to accommodate drift of data input streams being multiplexed.